AMENDMENT AND RESPONSE UNDER 37 CFR § 1.111

Serial Number: 10/616,810 Filing Date: July 10, 2003

Title: SLAVE-LESS EDGE-TRIGGERED FLIP-FLOP

Assignee: Intel Corporation



Page 1 Dkt1000-0011

<u>S/N 10/616,810</u> <u>PATENT</u>

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Mark E. Schuelein

Examiner: Minh Nguyen

Serial No.: 10/616,810

Group Art Unit: 2816

Filed:

July 10, 2003

Docket No.: 1000-0011

Title:

SLAVE-LESS EDGE-TRIGGERED FLIP-FLOP

AMENDMENT AND RESPONSE UNDER 37 CFR § 1.111

Mail Stop Amendment Commissioner for Patents P.O.Box 1450 Alexandria, VA 22313-1450

Applicant has reviewed the Office Action mailed on <u>June 29, 2004</u>. Please amend the above-identified patent application as follows.

IN THE DRAWINGS

A new drawing is supplied herewith. Enclosed is a copy of new Figure 4.

IN THE SPECIFICATION

Please amend the specification as follows:

The paragraph beginning at page 1, line 14 of the specification as filed is amended as follows: "Fig. 2 is a schematic diagram illustrating an example slave-less edge-triggered flip flop in accordance with another embodiment of the present invention; and".

The paragraph beginning at page 1, line 16 of the specification as filed is amended as follows: "Fig. 3 is a flowchart illustrating an example method for use in storing a data bit within a memory cell in accordance with an embodiment of the present invention; and[[.]]".

Please add the following new paragraph after the paragraph beginning at page 1, line 16 of the specification as filed: "Fig. 4 is a block diagram illustrating a computing system in accordance with an embodiment of the present invention."